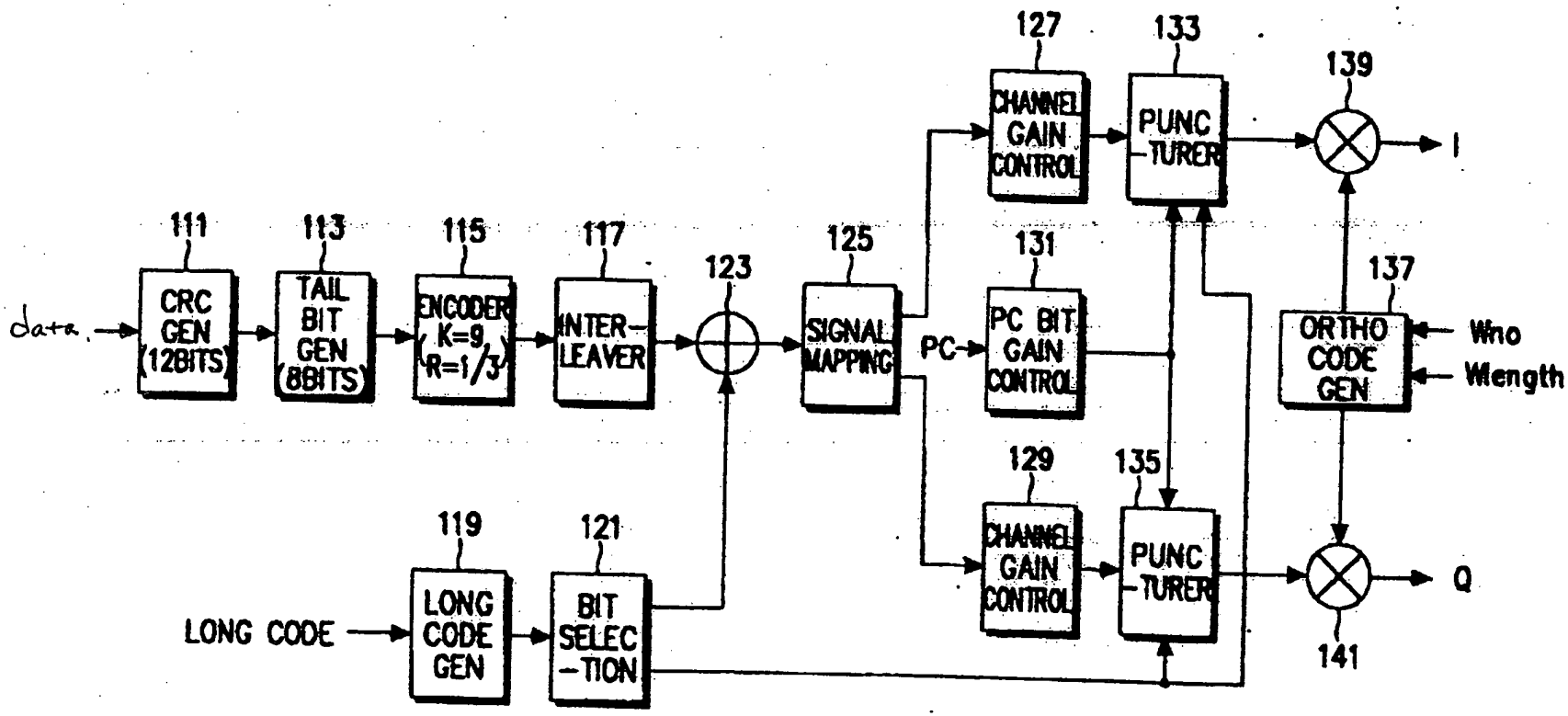

data
CRC GEN (12BITS)
TAIL BIT GEN (8BITS)
ENCODER K=9 R=1/3
INTER-LEAVER
SIGNAL MAPPING
PC
PC BIT GAIN CONTROL
CHANNEL GAIN CONTROL
PUNC-TURER
CHANNEL GAIN CONTROL
PUNC-TURER
ORTHO CODE GEN
Wno
Wength
I
Q
LONG CODE
LONG CODE GEN
BIT SELEC-TION
111
113
115
117
119
121
123
125
127
129
131
133
135
137
139
141

FIG. 1

FIG. 2a

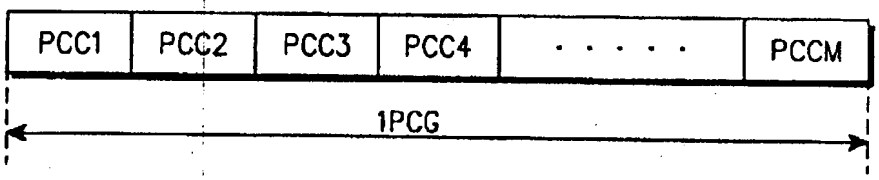

PCC1
PCC2
PCC3
PCC4
PCCM
1PCG

FIG. 2b

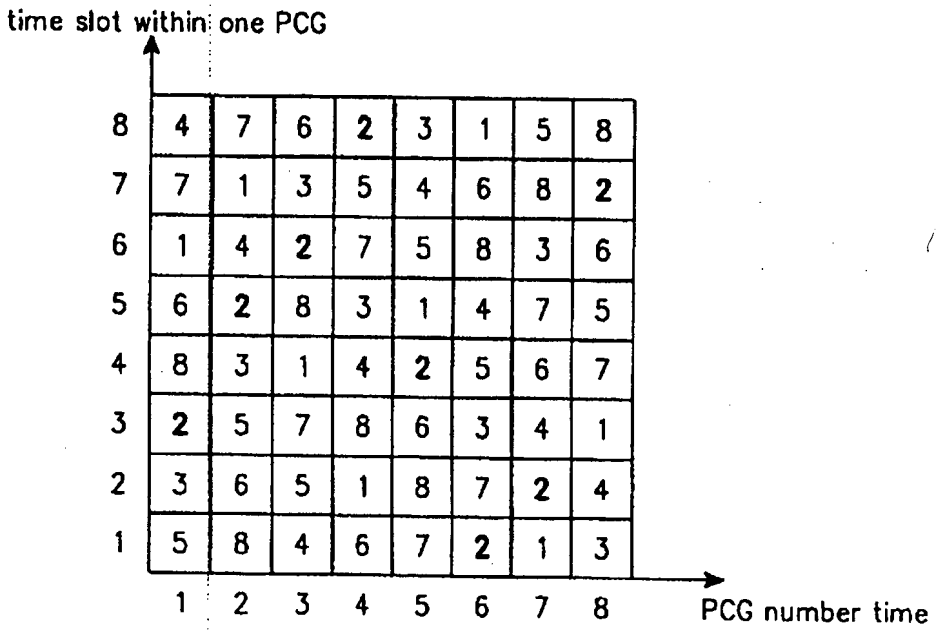
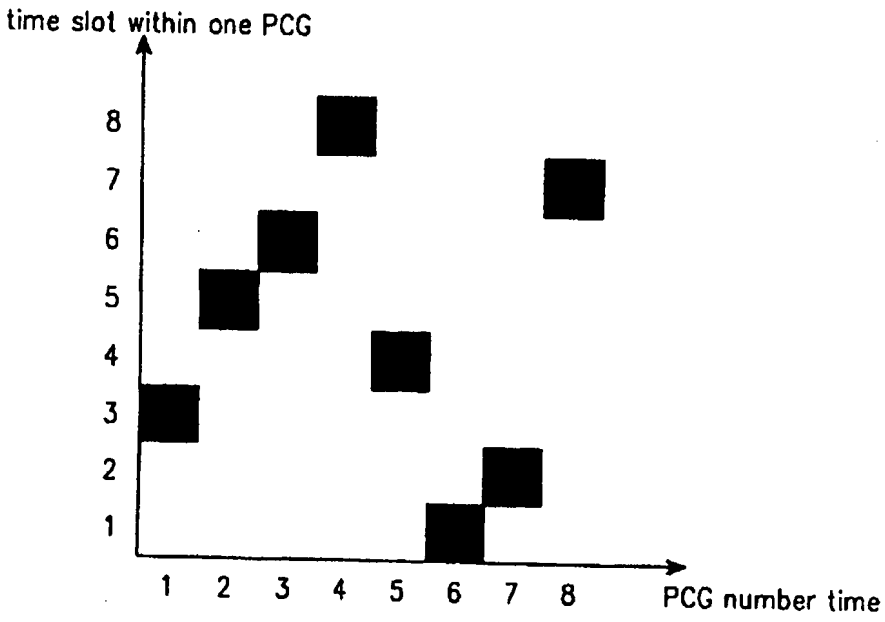

time slot within one PCG
8 4 7 6 2 3 1 5 8
7 7 1 3 5 4 6 8 2
6 1 4 2 7 5 8 3 6
5 6 2 8 3 1 4 7 5
4 8 3 1 4 2 5 6 7
3 2 5 7 8 6 3 4 1
2 3 6 5 1 8 7 2 4
1 5 8 4 6 7 2 1 3
1 2 3 4 5 6 7 8
PCG number time

FIG 2c



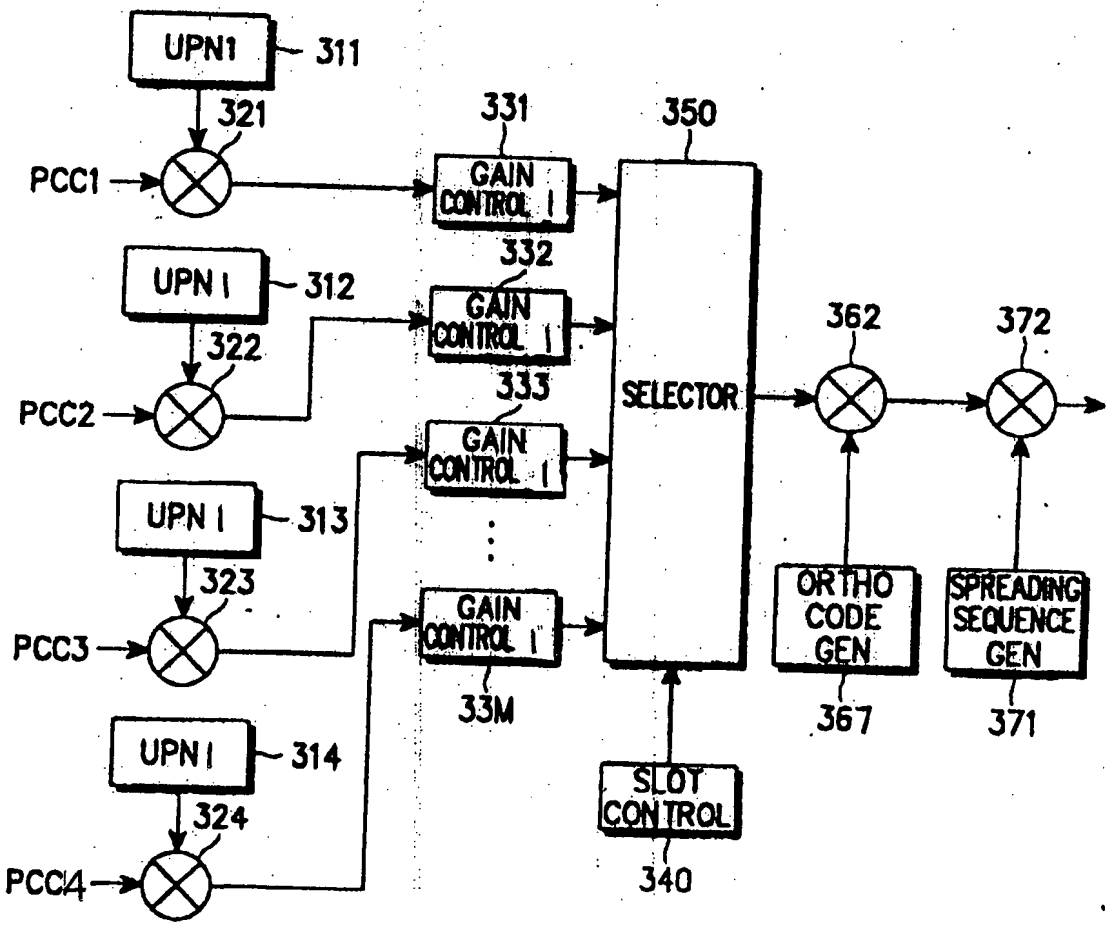

UPN1
311
321
PCC1
331
GAIN CONTROL 1
350
UPN 1
312
322
PCC2
332
GAIN CONTROL 1
333
GAIN CONTROL 1
SELECTOR
362
372
UPN 1
313
323
PCC3
GAIN CONTROL 1
33M
ORTHO CODE GEN
367
SPREADING SEQUENCE GEN
371
UPN 1
314
324
PCC4
SLOT CONTROL
340

FIG. 3

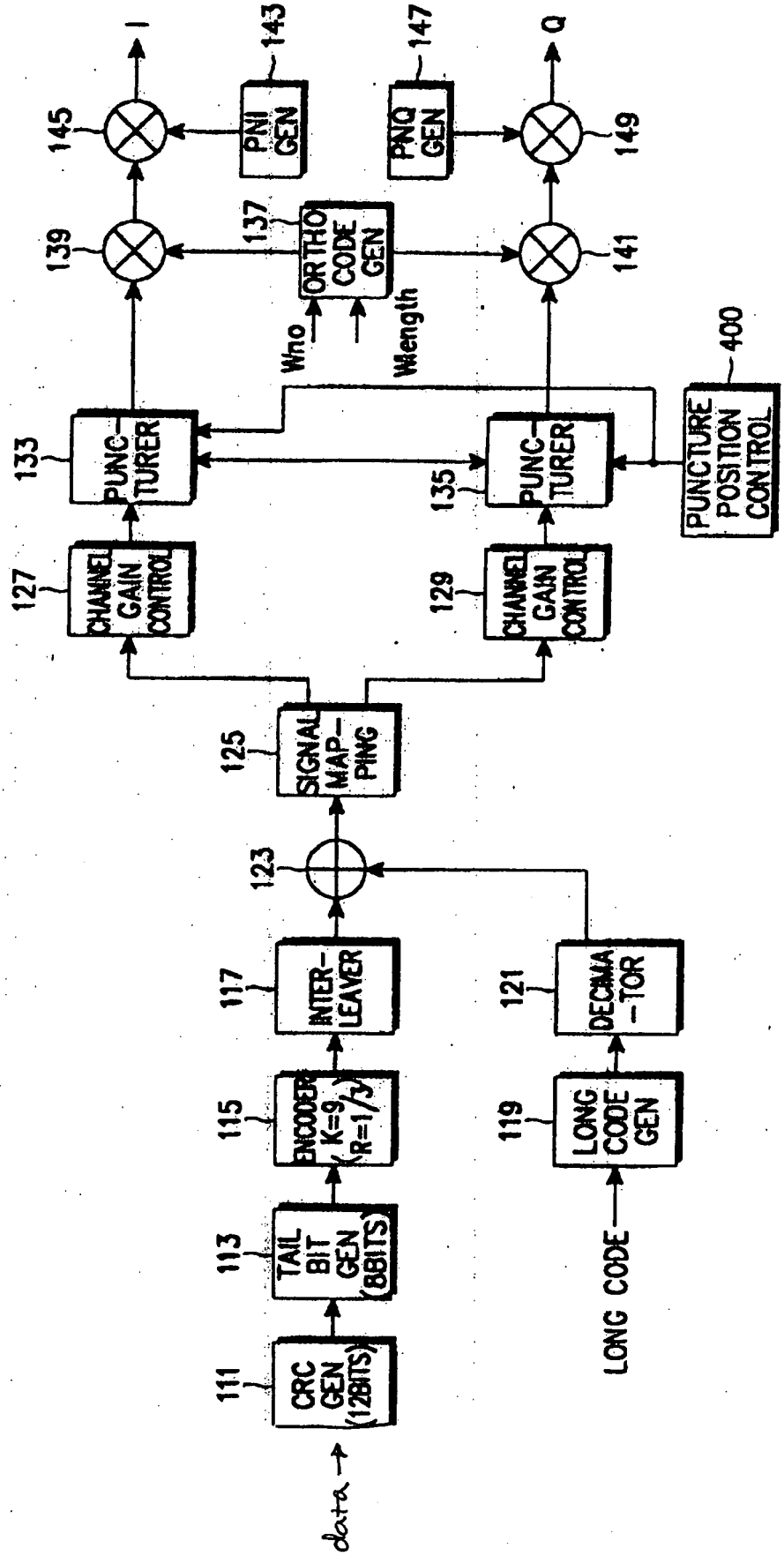

data
CRC GEN (12BITS)
111
TAIL BIT GEN (8BITS)
113
ENCODER (K=9, R=1/3)
115
INTER-LEAVER
117
123
SIGNAL MAP-PING
125
CHANNEL GAIN CONTROL
127
CHANNEL GAIN CONTROL
129
PUNC-TURER
133
PUNC-TURER
135
PUNCTURE POSITION CONTROL
400
Wno
Wength
ORTHO CODE GEN
137
139
141
PNI GEN
143
PNQ GEN
147
145
149
I
Q
LONG CODE GEN
119
DECIMA-TOR
121
LONG CODE

FIG. 4

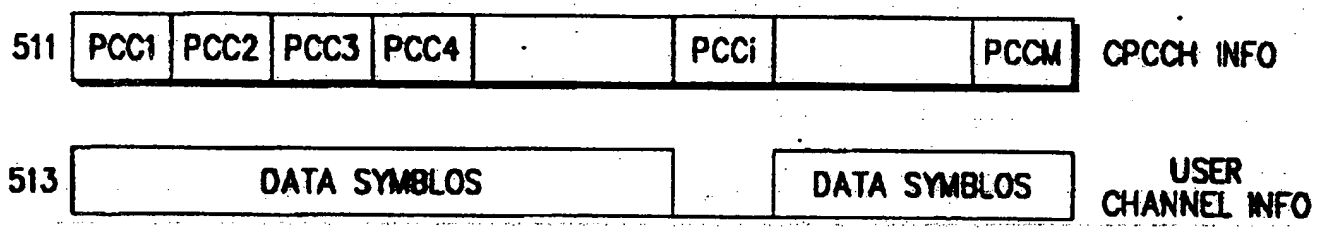

511
PCC1
PCC2
PCC3
PCC4
PCCi
PCCM
CPCCH INFO
513
DATA SYMBLOS
DATA SYMBLOS
USER CHANNEL INFO

FIG. 5

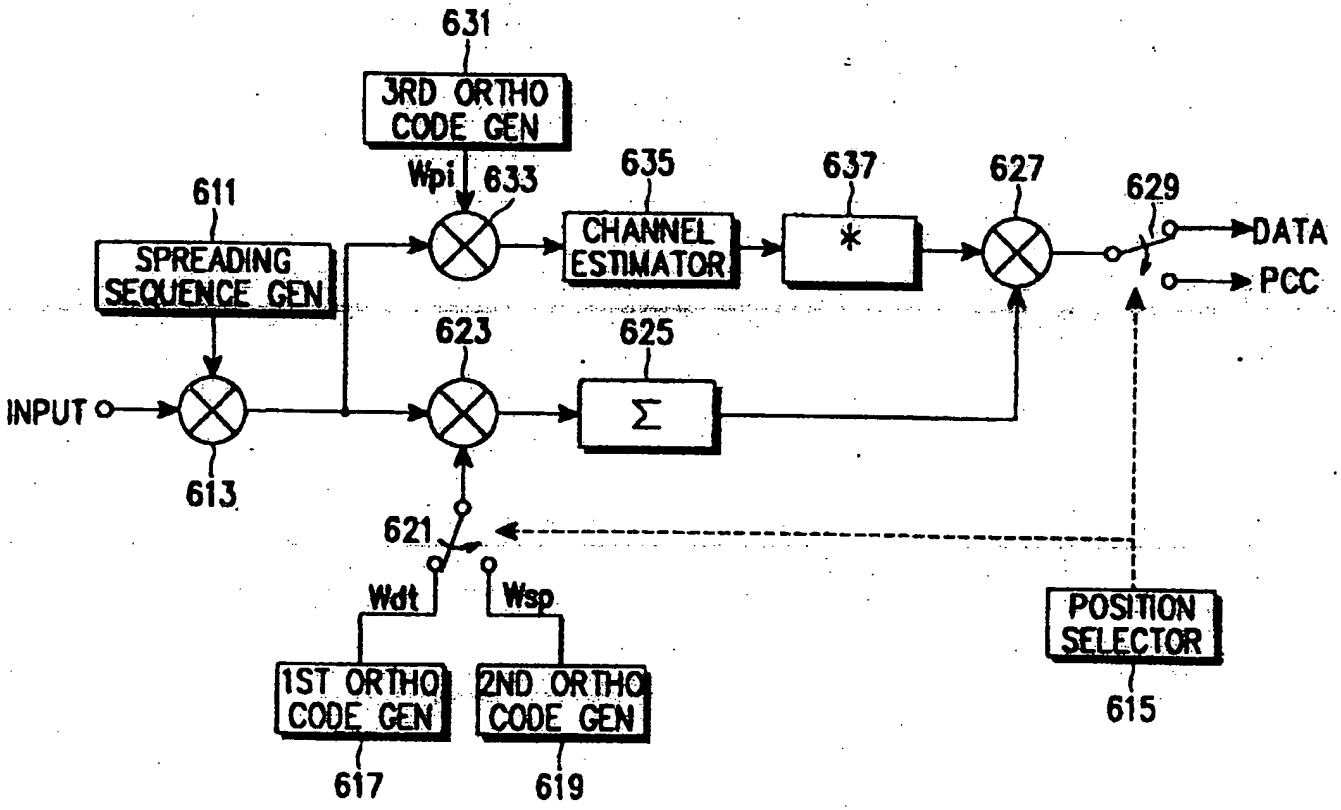

631
3RD ORTHO CODE GEN
Wpi
633
635
637
627
629
611
CHANNEL ESTIMATOR
*
DATA
PCC
SPREADING SEQUENCE GEN
623
625
INPUT
Σ
613
621
Wdt
Wsp
POSITION SELECTOR
1ST ORTHO CODE GEN
2ND ORTHO CODE GEN
615
617
619

FIG. 6